

# iXon<sup>EM</sup>+ 860 (back-illuminated)



low-light imaging

## Features & benefits

### EMCCD Technology

Ultimate in Sensitivity from EMCCD gain – even single photon signals are amplified above the noise floor. Full QE of CCD chip is harnessed (no intensifier).

### TE cooling to -100°C

Critical for elimination of darkcurrent detection limit.

### RealGain™

Absolute EMCCD gain selectable directly from a linear and quantitative scale.

### 515 full frames/sec

Ideal for highly dynamic, low light experiments.

### iCam

Unique innovation that empowers the EMCCD to operate with market-leading acquisition efficiency through live cell microscopy software.

> 90% QE back-illuminated sensor Maximum possible photon collection efficiency.

### Variable readout rates up to 10 MHz

Quantitative accuracy at all speeds and slower readout rate for enhanced 16-bit dynamic range.

### UltraVac™\*1

Critical for sustained vacuum integrity and to maintain unequalled cooling and QE performance, year after year.

### High dynamic range and 16-bit digitization available

Extended sensor dynamic range (readout speed dependent) and matched digitization for quantization of dim and bright signals.

### Minimal Clock-Induced Charge

Unique pixel clocking parameters, yielding minimized spurious noise floor.

### Cropped sensor mode

Specialised acquisition mode for continuous imaging with fast temporal resolution

### Enhanced Baseline Clamp

Essential for quantitative accuracy of dynamic measurements.

### Built-in C-mount compatible shutter (optional)

Easy means to record control dark images – excellent for optimization of experimental set-up.

“Lightning speed and ultra-sensitivity”

Andor's iXon<sup>EM</sup>+ 860 back-illuminated EMCCD is designed for very rapid imaging of low light events, combining > 500 frames/sec with single photon detection capability and > 90% Quantum Efficiency. UltraVac™ vacuum technology provides absolute protection of the back-illuminated sensor, ensuring performance longevity.



Thermoelectric cooling down to -100°C

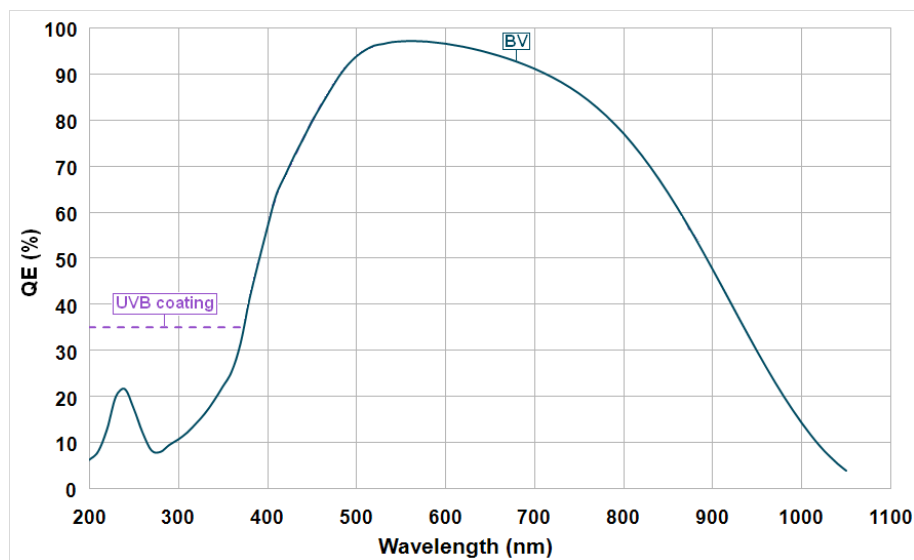
minimizes EM-amplified darkcurrent, whereas industry fastest vertical shift speeds minimizes both clock induced charge noise and vertical smear during frame transfer. The absolute EM gain multiplication can be varied linearly from unity up to a thousand times directly via RealGain™, a true quantitative EM gain scale.

Sub-millisecond biology is readily accessible through use of sub-array selection and pixel binning. The speed and sensitivity of the iXon<sup>EM</sup>+ 860 also renders it ideal for adaptive optics.

### Camera overview

Active Pixels	128 x 128
Pixel Size (W x H; μm)	24 x 24
Image Area (mm)	3.1 x 3.1
Active Area Pixel Well Depth (e <sup>-</sup> )	
Typical	160000
Maximum	220000
Gain Register pixel well depth (e <sup>-</sup> , typical)	800,000 <sup>+2</sup>
Max Readout Rate (MHz)	10
Frame Rates (frames per sec)	500 up to several thousands
Read Noise (e <sup>-</sup> )	< 1 to 48 @ 10 MHz

### Quantum efficiency<sup>+3</sup>



### Peak Quantum Efficiency (%)

CCD Type	Typical
BV @ 575 nm	92.5

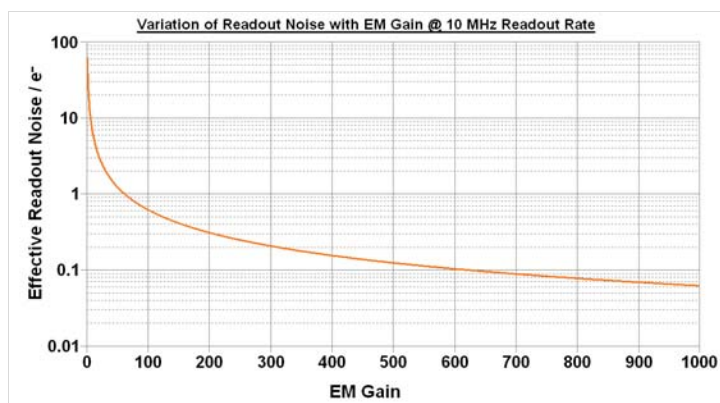
## Technical specifications

### System characteristics

<b>Pixel Readout Rate (MHz)</b>	
Electron Multiplying Amplifier	10, 5, 3, 1
Conventional Amplifier	3 & 1
<b>Digitization</b>	
True 14-bit @ 10, 5, 3 & 1 MHz readout rate (16 bit available @ 1 MHz)	
<b>Vertical Clock Speed (μs)</b>	
0.0875 to 0.45 (variable)	
<b>Linear Absolute Electron Multiplier Gain</b>	
1 - 1000 times (software controlled)	
<b>Linearity (% , maximum)<sup>*4</sup></b>	
1	
<b>Triggering</b>	
Internal, External, External Start	
<b>Camera window type</b>	
Single window with double-sided AR coating (standard for BV model)	

### System Readout Noise (e<sup>-</sup>)<sup>\*5</sup>

	Typical	With Electron Multiplication
10 MHz through EMCCD amplifier	48	<1
5 MHz through EMCCD amplifier	40	<1
3 MHz through EMCCD amplifier	28	<1
1MHz (16-bit) through EMCCD amplifier	18	<1



### Minimum sensor temperatures (typical)

Air cooled (ambient air at 20°C)	-85°C
Water cooled using Re-circulator (ambient air @ 20°C)	-90°C
Water cooled using Chiller (@ 10°C, 0.75 l / min)	-100°C

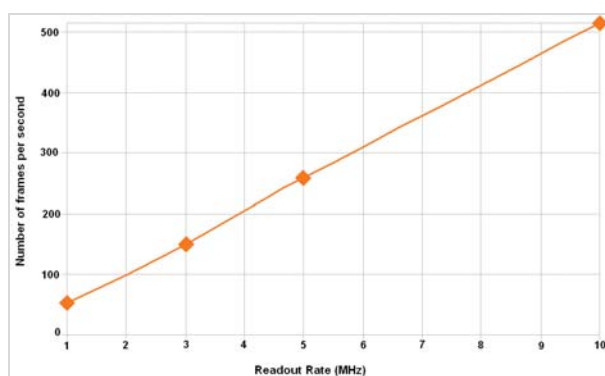
### Dark Current & Background Events

Dark Current @ -85°C (e <sup>-</sup> /pix/sec) <sup>*6</sup>	0.002
EMCCD-Amplified Background Events <sup>*7</sup> (events/pix @ 1000 x gain and -85°C)	0.02

### Blemish specification

As defined by the sensor manufacturer e2v.  
Can be accessed in the CCD60 back-illuminated sensor datasheet, downloadable from this web page: <http://www.e2v.com/module/page-357/l3-vision-datasheets-and-technical-notes.cfm>

### Full frame rate<sup>\*8</sup>



### Max frames per second<sup>\*9</sup>

Binning	Array size			
	128 x 128 (Full Frame)	64 x 64	32 x 32	128H x 50V
1 x 1	515	943	1613	1163
1 x 2	943	1613	2500	1923
2 x 2	943	1613	2500	1923
1 x 4	1613	2500	3571	2941
4 x 4	1613	2500	3571	2941

### Computer requirements

To handle data transfer rates of 10 MHz readout over extended kinetic series, a powerful computer is recommended, e.g.:

- 3 GHz Pentium (or better)
- 1GB RAM
- 10,000 rpm SATA hard drive preferred for extended kinetic series
- PCI-compatible computer. PCI slot must have bus master capability.
- Available auxiliary internal power connector
- 32 MB free hard disc space

### Operating & storage conditions

Operating Temperature	0°C to 30°C ambient
Relative Humidity	< 70% (non-condensing)
Storage Temperature	-25°C to 55°C

### Power requirements<sup>\*10</sup>

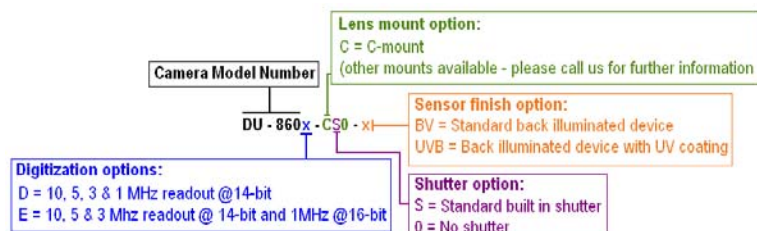
- 0.6A @ +12V
- 0.3A @ -12V
- 3.0A @ +5V

### Need more information? Please contact us at:

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## Ordering information & notes

To order the camera you require, please use the following ordering system:



E.g. a DU-860D-CS0-UVB is a back-illuminated iXon<sup>EM</sup>+ 860 camera with 14-bit digitization at 10, 5, 3 and 1 MHz readout speeds, EMCCD & Conventional output amplifiers, standard shutter and UV-enhanced coating.

The iXon<sup>EM</sup>+ 860 requires the following controller card:

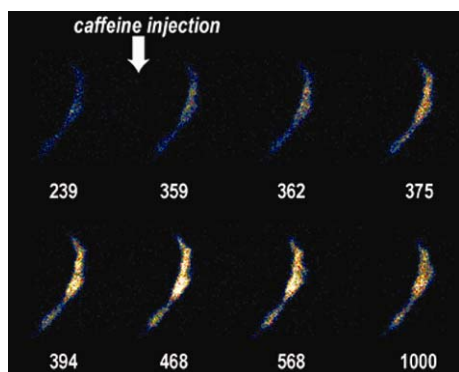
CCI-23 PCI controller card

The iXon<sup>EM</sup>+ 860 also requires one of the following software options:

<b>Andor Solis (i)</b>	A ready-to-run Windows 2000 or XP-based package with rich functionality for data acquisition and processing.
<b>Andor SDK</b>	A DLL driver and software development kit that let you create your own applications for the Andor Camera. Available for Windows 2000 or XP and Linux.
<b>Andor iQ</b>	A comprehensive multi-dimensional imaging software package. Offers tight synchronization of EMCCD with a comprehensive range of microscopy hardware, along with comprehensive rendering and analysis functionality. Modular architecture for best price/performance package on the market.
<b>Third party software compatibility</b>	Drivers are available so that the iXon <sup>EM</sup> + range can be operated through a large variety of third party imaging packages.

The following accessories are available for use with the iXon<sup>EM</sup>+ 860:

<b>XW-RECR</b>	Re-circulator for enhanced cooling performance
<b>XW-CHIL-150</b>	Chiller/re-circulator for maximum cooling performance
<b>REMOTE CTRL KIT</b>	Programmable remote control for controlling functions from anywhere around the optical set-up



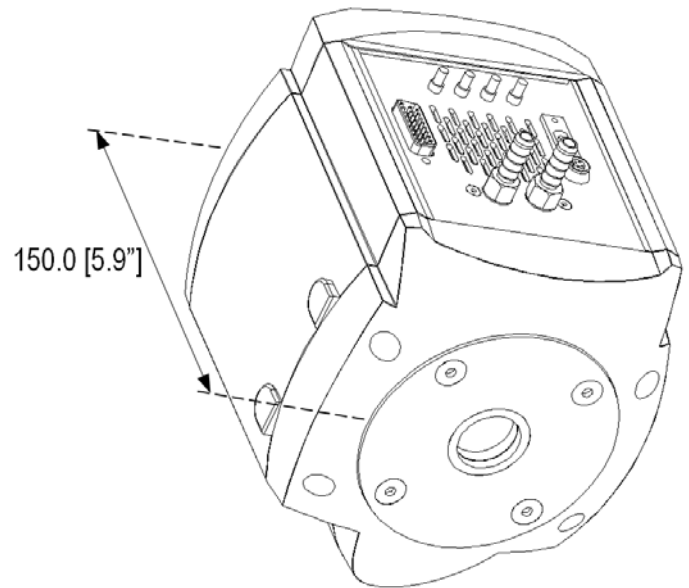
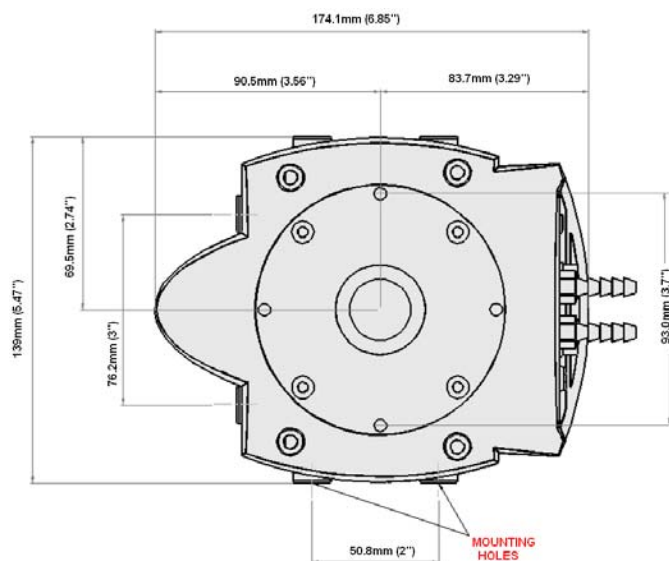
Selected frames from a kinetic series, showing Ca<sup>2+</sup> flux change in smooth muscle cells recorded with Andor iXon<sup>EM</sup>+ at 100 fps full frame.

Specifications are subject to change without notice

- ◆1 Assembled in a state-of-the-art Class 10,000 cleanroom facility, Andor's UltraVac™ vacuum process combines a permanent hermetic vacuum seal (no o-rings), with a stringent protocol to minimize outgassing, including use of proprietary materials. Outgassing is the release of trapped gases that would otherwise prove highly problematic for high-vacuum systems.
- ◆2 The EM register on CCD60 sensors has a linear response up to 400,000 electrons maximum and a full well depth of ~800,000 electrons maximum.
- ◆3 Quantum efficiency of the CCD sensor as measured by the CCD Manufacturer.
- ◆4 Linearity is measured from a plot of Counts vs. Signal up to the saturation point of the system. Linearity is expressed as a percentage deviation from a straight line fit.
- ◆5 System Readout noise is for the entire system. It is a combination of CCD readout noise and A/D noise. Measurement is for Single Pixel readout with the CCD at a temperature of -85°C and minimum exposure time under dark conditions. Under Electron Multiplying conditions, the effective system readout noise is reduced to sub 1e<sup>-</sup> levels. Noise values will change with pre-amplifier gain (PAG) selection. Values quoted are measured with highest available PAG setting.
- ◆6 This value is obtained using the traditional method of measuring dark current, as for any CCD camera, i.e. taking a long integration time (with no EM gain applied) to get a dark signal that is well above the read noise. The dark current measurement is averaged over the CCD area excluding any regions of blemishes.
- ◆7 Using Electron Multiplication (EM) the iXon<sup>EM</sup>+ is capable of detecting single photons, therefore the true camera detection limit is set by the number of "dark" background events. These background events consist of both residual thermally generated electrons and Clock Induced Charge (CIC) electrons (also referred to as Spurious Charge), each appearing as random single spikes that are well above the read noise floor. A thresholding scheme is employed to count these single electron events and is quoted as a probability of an event per pixel. Acquisition conditions are full resolution and max frame rate (10 MHz readout; frame-transfer mode; 0.3μs vertical clock speed; x 1000 EM gain; 30ms exposure; -85°C). It is important to realise that to get to this single photon detection regime there must be sufficient cooling, such that there is significantly less than 1 event per pixel.
- ◆8 The graph shows the full frame rates possible when reading out the sensor at 10, 5, 3 and 1 MHz pixel readout rates, and using 0.3μs vertical clock speed.
- ◆9 The max frames / second for iXon<sup>EM</sup>+ imaging CCDs is the maximum speed at which the device can acquire images in a standard system. Shown are the frame rates at 10 MHz digitization rates for a range of binning or array size combinations. All measurements are made with 0.3μs vertical clock speed. It also assumes internal trigger mode of operation
- ◆10 These power requirements are the maximum load that will be drawn from the computer for the camera head and controller card combined.

## Dimensions

Weight: 3.4 kg [7.5 lb]



## Notes:

1. The clearance from the C-mount face plate to the shutter is 6mm. Please ensure that when fitting a lens, to a system with a built in shutter, that it does not extend into the housing by more than 5mm.
2. There are mounting holes (¼-20UNC) located on three sides of the camera. They are positioned centrally at a distance of 40mm from the front of the front face.

## Connections

